

EUROPEAN PATENT APPLICATION

Application number: 87310140.6

Int. Cl.⁴: **H04H 1/00** , **H04S 1/00** ,
H04B 1/66

Date of filing: 17.11.87

Priority: 24.11.86 GB 8628046

Date of publication of application:
 06.07.88 Bulletin 88/27

Designated Contracting States:
AT BE CH DE ES FR GB GR IT LI LU NL SE

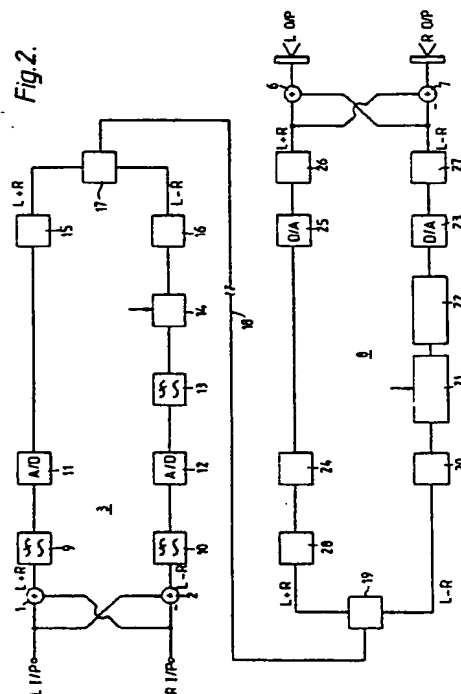
Applicant: **BRITISH TELECOMMUNICATIONS plc**
British Telecom Centre 81 Newgate Street
London EC1A 7AJ(GB)

Inventor: **Holt, Christopher Ellis**
3 Lark Rise Martlesham Heath
Ipswich Suffolk, IP5 7SA(GB)
 Inventor: **Rollett, John Mortimer**
29 Park Road
Ipswich Suffolk, IP1 3SX(GB)

Representative: **Lloyd, Barry George William et al**
Intellectual Property Unit British Telecom
Room 1304 151 Gower Street
London WC1E 6BA(GB)

A transmission system.

A stereo coding system is described in which 7kHz bandwidth sum (L+R) and difference (L-R) signals are converted to digital form (11 and 12). The difference signal is then bandlimited (13) to 2kHz and downsampled (14) to 4k samples/sec prior to being encoded (16), and the sum signal is encoded (15); both are then multiplexed together (17) prior to transmission. After reception, the signal is demultiplexed (19) and the difference signal is decoded (20) upsampled (21) to its original bit rate and filtered to prevent aliasing (22), and the sum signal is delayed in a digital buffer (28) and decoded (24) prior to both being converted back to analogue form (23 and 25). The delay introduced by the digital buffer (28) equalises the excess delay introduced into the difference signal.



Xerox Copy Centre

EP 0 273 567 A1

A TRANSMISSION SYSTEM

This invention relates to a transmission system for audio signals; and a transmitter and receiver for use in such a system.

UK Patent No: 970,051 describes a known stereo coding system (illustrated in Figure 1) which derives sum $L+R$ and difference $L-R$ signals from the left L/P and right R/P inputs, for example using interconnected adders 1 and 2. The sum channel carries the sum signal - a monaural signal; and the difference channel carries the information that determines the relative phase and magnitude of the two outputs.

The human listener utilizes both phase and amplitude information to localize sounds. It is known that the relative phase of the signals reaching the ears is significant up to about 1kHz; whilst the relative amplitudes of the left and right signals are significant up to about 10kHz.

If, in the transmitter 3, the difference signal is band-limited, say to 5kHz as suggested in the above mentioned UK Patent, by the use of a low-pass analogue filter 4, the majority of the useful localisation (phase and amplitude) information is retained, and a substantial reduction in the transmission link 5 bandwidth is achieved. When the sum and difference signals are mixed, 6 and 7, at the receiving end 8 to recreate the "left" and "right" signals the brain can compensate for the lost information in the difference channel and still form a stereophonic image.

The main disadvantage of such band-limiting systems is that the band-limiting low-pass filter introduces phase distortion into the difference signal relative to the sum signal, resulting in loss of stereo information. The use of analogue filters to provide a corresponding phase compensation into the sum signal is difficult to achieve accurately and causes, in turn, the introduction of noise into that signal's amplitude which leads to degradation of the system performance.

According to a first aspect of the present invention there is provided a transmission system for audio signals in which at a transmitter: a monophonic signal and a difference signal are derived from left and right input signals, a digital version of the difference signal is bandlimited to a predetermined bandwidth smaller than that of the monophonic signal, the sampling rate of the digital bandlimited difference signal is reduced, and the monophonic and resulting difference signals are transmitted to a receiver; and in which at a receiver: upon reception, the sampling rate of the digital difference signal is increased, frequencies above the predetermined bandwidth in the difference signal are attenuated, and left and right output

signals are derived from the monophonic and resultant difference signals; and in which, either at the transmitter or receiver, compensation for the differential delays in the monophonic and difference signals is introduced.

According to a second aspect of the invention there is provided a transmitter for use in a transmission system according to the first aspect comprising: a means for receiving left and right input signals, and for forming a monophonic signal and a digital difference signal therefrom; a digital filter for bandlimiting the digital difference signal to a predetermined bandwidth smaller than that of the monophonic signal; and a downsampler for reducing the sampling rate of the filtered difference signal.

According to a third aspect of the invention there is provided a receiver for use in a transmission system according to the first aspect comprising: receiving means for receiving monophonic signals, and for receiving digital difference signals having a predetermined maximum bandwidth less than that of the monophonic signals; an upsampler for increasing the sampling rate of the difference signals; a digital filter for attenuating frequencies above the said predetermined maximum bandwidth in the upsampled difference signal; and digital-to-analogue conversion and mixing means for forming analogue left and right output signals from the monophonic and resultant difference signals.

According to a fourth aspect of the invention there is provided a transmission apparatus for stereophonic audio signals comprising: means for forming monophonic and difference signals from left and right input signals; means for bandlimiting the difference signal to substantially 2kHz; and means for transmitting the monophonic and difference signals.

It has been found that acceptable results can be obtained when the difference signal is bandlimited to 2kHz when the invention is exercised since the maximum amount of phase information is preserved in the remaining frequency range.

The compensating means, for compensating differential delays in the sum and difference signals, is preferably included in the receiver. And since the overall delay is usually greater in the difference channel the compensating means is usually included in the sum channel. Delay compensation is advantageously performed digitally, say by a digital buffer, so the sum signal is often also covered to digital form, either in the transmitter or receiver.

This invention will now be described by way of example with reference to Figures 2 to 4 of the

accompanying drawings in which:

Figure 2 illustrates a block diagram of a system embodying the invention;

Figure 3 illustrates the coder as used in a preferred embodiment of the invention; and

Figure 4 illustrates a decoder as used in a preferred embodiment of the invention;

Figure 5 illustrates a block diagram of another system embodying the invention;

Figure 5a illustrates an alternative input weighting scheme for the system shown in figure 5.

Referring now to Figure 2, sum $L+R$ and difference $L-R$ signals are again derived, using interconnected adders 1 and 2, from the left and right input signals at the front end of the transmitter 3. Both sum and difference signals are then filtered to, say, a bandwidth of 7kHz by analogue filters, 9 and 10 - which are preferably identical so as not to introduce differential delays - before being digitized, with a sampling rate of 16k samples/sec, in analogue-to-digital converters 11 and 12 (although of course the filters could precede the interconnected adders 1 and 2).

The digital difference signals is then band-limited, to say 2kHz, by a digital low pass filter 13, which preferably has a delay characteristic independent of frequency. The band-limited difference signal is then down-sampled to a sampling rate of 4k samples/sec in down sampler 14. This bit rate reduction is possible because the nyquist frequency of the difference signal has been reduced to 4kHz by the band limiting.

The sum and difference signals are then coded by digital coders 15 and 16 in conventional fashion, eg using adaptive differential pulse code modulation (ADPCM). The sum and difference signals may then be multiplexed together in multiplexer 17 before transmission over the digital transmission link 18 to a receiver. At the receiver 8 the received digital signal may be demultiplexed in demultiplexer 19 to obtain the digital sum and difference signals; these are then processed in separate channels.

The difference signal is decoded by decoder 20 to produce the 2kHz band-limited digital difference signal. This signal is then up-sampled back to its original bit rate in upsampler 21 and filtered by a low-pass (2kHz cut-off) filter 22 to avoid aliasing. The difference signal is then converted to analogue form by digital-to-analogue converter 23.

Meanwhile the sum signal is decoded in decoder 24 and converted to analogue form by digital-to-analogue converter 25. Both sum and difference signals are filtered separately, but preferably identically, by low-pass filters 26 and 27 prior to mixing (6 and 7) to produce "left" and "right" outputs.

The coder and decoder differ in their treatment

of the sum and difference signals in that the difference signals are band limited, down-sampled, upsampled and filtered to remove aliasing.

All these processing steps take place while the sum and difference signals are in digital form. It is relatively easy to construct digital filters having a constant delay response, so that the resultant relative delay, due to the bandlimiting of the difference signal, and any other delays can be compensated for by a digital delay 28 in the sum channel path (or the other channel if appropriate) at any point where the sum signal is in digital form, but preferably between the demultiplexer 19 and decoder 24 in the receiver 8. The digital delay 28 may be realised in known fashion to introduce the precise amount of delay required to equalize the overall delays of both channels without corrupting the signals in any other way.

The "left" and "right" outputs are only true stereo in the 0-2kHz frequency range, outside this range the two output signals are identical. For this reason the sum signal need not in fact be a sum $L+R$ signal above 2kHz, but may be any monophonic representation of the input or the left or right input signal alone, in this frequency range.

Referring now to Figures 3 and 4, a second embodiment of the invention is shown in which stereo input signals are encoded so as to be transmitted over a standard 64kbit/s digital link. The link (which is the subject of a draft CCITT standard) utilises 56kbit/s for a speech channel using two-band sub-band coding plus ADPCM, multiplexed with an 8kbit/s side-channel. The same reference numerals as in Figure 2 have been used to designate corresponding elements of the system.

In the coder, Figure 3, sum and difference signals are again derived and processed by identical analogue filters 9 and 10 to produce signals having a bandwidth from 150Hz to 7kHz. Both are then converted to digital form by analogue-to-digital converters 11 and 12, having a sampling frequency of 16kHz.

The 7kHz signal band digital difference signal is then band limited by a digital low pass filter 13 to 2kHz. The difference signal is then downsampled to 4k samples/sec 14 and encoded using two-bit ADPCM 16 to produce an 8kbit/sec difference signal.

The sum signal, after conversion to digital form, is split into two 4 kHz channels (0-4kHz and 4-8kHz) by a pair of mirror filters 29, and each channel downsampled to 8k samples/sec by downsamplers 30 and 31 respectively. The sum signals are coded and using 5bit and 2bit ADPCM coders 32 and 33 respectively to produce a 40kbit/sec and a 16kbit/sec signal which are multiplexed together 34 to produce a 56kbit/sec sum signal.

The encoded sum and difference signals are

then multiplexed together 17 to provide a 64kbit/sec signal. The combined signal is formatted as a series of 8-bit bytes each containing 5bits sum signal/lower sub-band, 2bits sum signal/upper sub-bands and one bit difference signal.

At the decoder, Figure 4, the received 64kbit/sec signal is demultiplexed 19 to provide the 56kbit/sec sum signal and the 8kbit/sec difference signal. The difference signal is then decoded in ADPCM decoder 20 to provide a signal at 4k samples/sec which is upsampled to 16k samples/sec 21 and filtered by a 2kHz cut-off low pass filter 22 to remove aliasing.

The sum signal is delayed by a time t in buffer 28 and demultiplexed again 35 to provide a 40kbits/s and a 16kbits/s signal to 5bit and 2bit decoders, 36 and 37, respectively which produce two 4kHz bandwidth sum signals.

These are upsampled to 16k samples/sec, 38 and 39 and are filtered by mirror filters 40 to provide two 4kHz bandwidth signals representing the bands 0-4kHz and 4-8kHz. These are combined 41 to provide a sum signal to the digital-to-analogue converter 25.

The sum and difference signals are then processed in analogue form by identical filters 26 and 27 before mixing to reproduce the left and right outputs. The delay t introduced into the sum signal path is adjusted to equal the extra end-to-end delay introduced into the difference signal by the extra digital processing it undergoes, less any additional delay to the sum channel due to the sub-band coding/decoding.

The hardware and firmware required to implement the digital processing of this coding system may be reduced by timesharing between the sum and difference channels. In particular the filters 13 and 29 in the coder, and 22 and 40 in the decoder can share time between the left and right channels; and the coders 16,32 and 33 in the transmitter and decoders 21,38 and 39 in the receiver can share time.

It should be appreciated that although the invention has been described with reference to stereo audio transmission systems it is also applicable to quadrophonic and other multiple channel audio systems.

The effects of the loss of stereo information in the range 2-7kHz, although not particularly significant, can be mitigated by adjusting the instantaneous power ratio (balance) of the left and right output signals in this range to create a pseudo-stereo effect. The power ratio in this range is usually 50:50 and the adjustment is made by amplifying one (either the left or right) output signal and attenuating the other by a corresponding amount. The degree of adjustment may be controlled to ensure that the resulting power ratio in

this frequency range is the same as the power ratio of the left and right output signals in the 0-2KHz range (where there is true stereo). Alternatively a data signal carrying information about the degree of adjustment desired may be sent from the transmitter along with the sum and difference signals. This data signal may be generated at the transmitter from information about the relative positions of sound sources. For example, referring to Figure 5, if the source of sound is eight speakers at an audio conference terminal each equipped with a throat microphone M1-M8 then the transmitter codes the eight inputs into stereo sum and difference signals by weighting the inputs from the microphones M1-M8 by different factors (an equivalent weighting scheme is shown in figure 5a). The sum and difference signals are then coded 42,43 including bandlimiting the difference signal to 2kHz as described above, before transmission. However, in addition the transmitter comprises activity detection means 44 which determines the current mean position of the sound inputs from the eight microphones, and this is used to generate a data signal carrying information about the degree of sound balance of the input signals. The data signal is then coded 45 and multiplexed with the sum and difference signals 17 before being transmitted to the receiver.

At the receiver the received signal is demultiplexed 19 into the coded sum, difference and data signals. The sum and difference signals are decoded as previously described, 46,47, to produce an analogue sum signal of 7kHz bandwidth and an analogue difference signal of 2kHz bandwidth. The sum signal is split into a low band signal $(L+R)_l$ by a low-pass filter 48 having a cut-off frequency of 2kHz (ie equal to the bandwidth difference signal), and into a high-band signal $(L+R)_h$ by a band pass filter 49 having a pass band of 2-7kHz (ie the remainder). The low band sum signal $(L+R)_l$ and the difference signal are then mixed together in adders 6 and 7 to provide true stereo left LI and RI outputs of bandwidth 2kHz. The high band sum signal $(L+R)_h$ is then reintroduced, in adders 50 and 51, to the stereo signals.

Meanwhile, the coded data signal derived from the demultiplexer 19 is decoded 52 to produce the data signal S. This signal is applied to amplifiers 53 and 54 to control the amplification/attenuation factor of each in reciprocal fashion so that the high band sum signal $(L+R)_h$ is reintroduced in the desired ratio. The "left" and "right" outputs in the range 0-2kHz from the adders 50 and 51 will then still be true stereo, but in the range 2-7KHz will be the sum signal with appropriately adjusted balance.

Claims

1 A transmission system for audio signals in which at a transmitter: a monophonic signal and a difference signal are derived from multiple input signals, a digital version of the difference signal is bandlimited to a predetermined bandwidth smaller than that of the monophonic signal, the sampling rate of the digital bandlimited difference signal is reduced; and the monophonic and resulting difference signals are transmitted to a receiver; and in which at a receiver: upon reception, the sampling rate of the digital difference signal is increased, frequencies above the predetermined bandwidth in the difference signal are attenuated, and left and right output signals are derived from the monophonic and resultant difference signals; and in which, either at the transmitter or receiver, compensation for the differential delays in the monophonic and difference signals is introduced.

2 A transmitter for use in a transmission system according to claim 1, comprising: a means for receiving multiple input signals, and for forming a monophonic signal and a digital difference signal therefrom; a digital filter for bandlimiting the digital difference signals to a predetermined bandwidth smaller than that of the monophonic signal; and a downsampler for reducing the sampling rate of the filtered difference signal.

3 A transmitter according to claim 2, further comprising compensating means for compensating differential delays in the monophonic and difference signals.

4 A transmitter according to claim 3, further comprising analogue-to-digital converter means for providing the monophonic signal in digital form, wherein the compensating means is a digital delay for introducing a delay into the monophonic signal.

5 A transmitter according to claim 4 further comprising two identical analogue filters for filtering the monophonic and difference signals prior to conversion to digital form.

6 A transmitter according to claim 4 or 5 wherein both the digital monophonic and difference signals are provided at the same sampling rate.

7 A transmitter according to any one of claims 2 to 6 wherein the said predetermined bandwidth is substantially 2kHz.

8 A transmitter according to any one of claims 2 to 7 further comprising means for determining which of the multiple input signals is currently the dominant source of sound and means for generating a data signal carrying information about the adjustment of the power ratio of the left and right output signals in the frequency range above said predetermined maximum bandwidth that is desired for that input signal.

9 A receiver for use in a transmission system according to claim 1, comprising: receiving means for receiving monophonic signals, and for receiving digital difference signals having a predetermined maximum bandwidth less than that of the monophonic signals; an upsampler for increasing the sampling rate of the difference signals; a digital filter for attenuating frequencies above the said predetermined maximum bandwidth in the upsampled difference signal; and digital-to-analogue conversion and mixing means for forming analogue left and right output signals from the monophonic and resultant difference signals.

10 A receiver according to claim 9 comprising compensating means for compensating differential delays in the monophonic and difference signals.

11 A receiver according to claim 10 wherein the monophonic signals also received in digital form and the compensating means is a digital delay for introducing a delay into the monophonic signal.

12 A receiver according to claim 11 further comprising two identical analogue filters for filtering the left and right output signals.

13 A receiver according to any one of claims 9 to 12 wherein the said predetermined maximum bandwidth is substantially 2kHz.

14 A receiver according to claim 11, 12 or 13 wherein the upsampled difference signal and the received monophonic signal have the same sampling rate.

15 A receiver according to any one of claims 9 to 14 also comprising means for adjusting the power ratio of the left and right output signals in the frequency range above said predetermined maximum bandwidth in accordance with the relative power of the left and right output signals in the frequency range below said predetermined maximum bandwidth.

16 A receiver according to any one of claims 9 to 14 also comprising means for adjusting the power ratio of the left and right output signals in the frequency range above said predetermined maximum bandwidth in accordance with information carried in a received data signal.

17 A transmission apparatus for stereophonic audio signals comprising: means for forming monophonic and difference signals from left and right input signals; means for bandlimiting the difference signal to substantially 2kHz; and means for transmitting the monophonic and difference signals.

Fig.1.

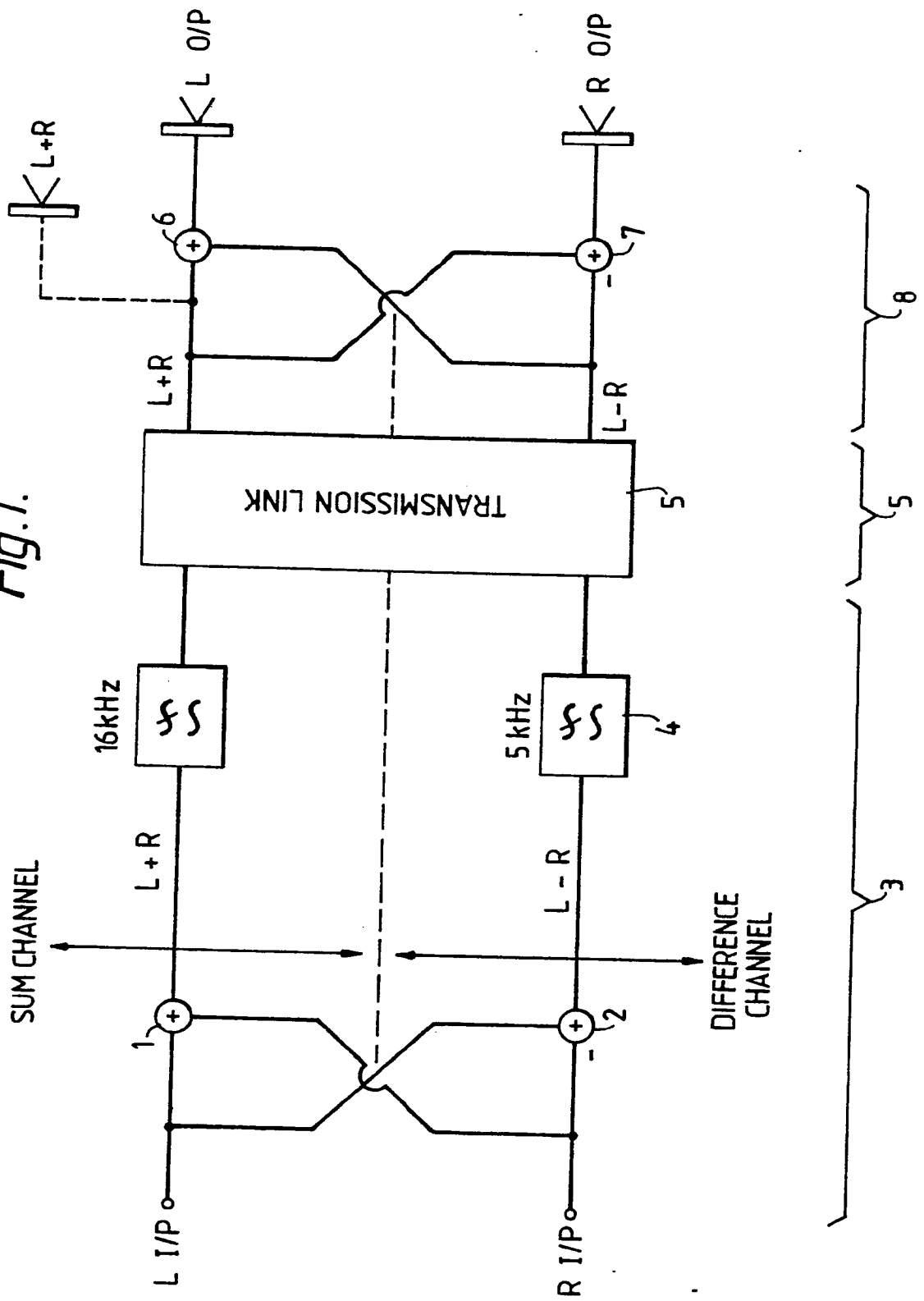
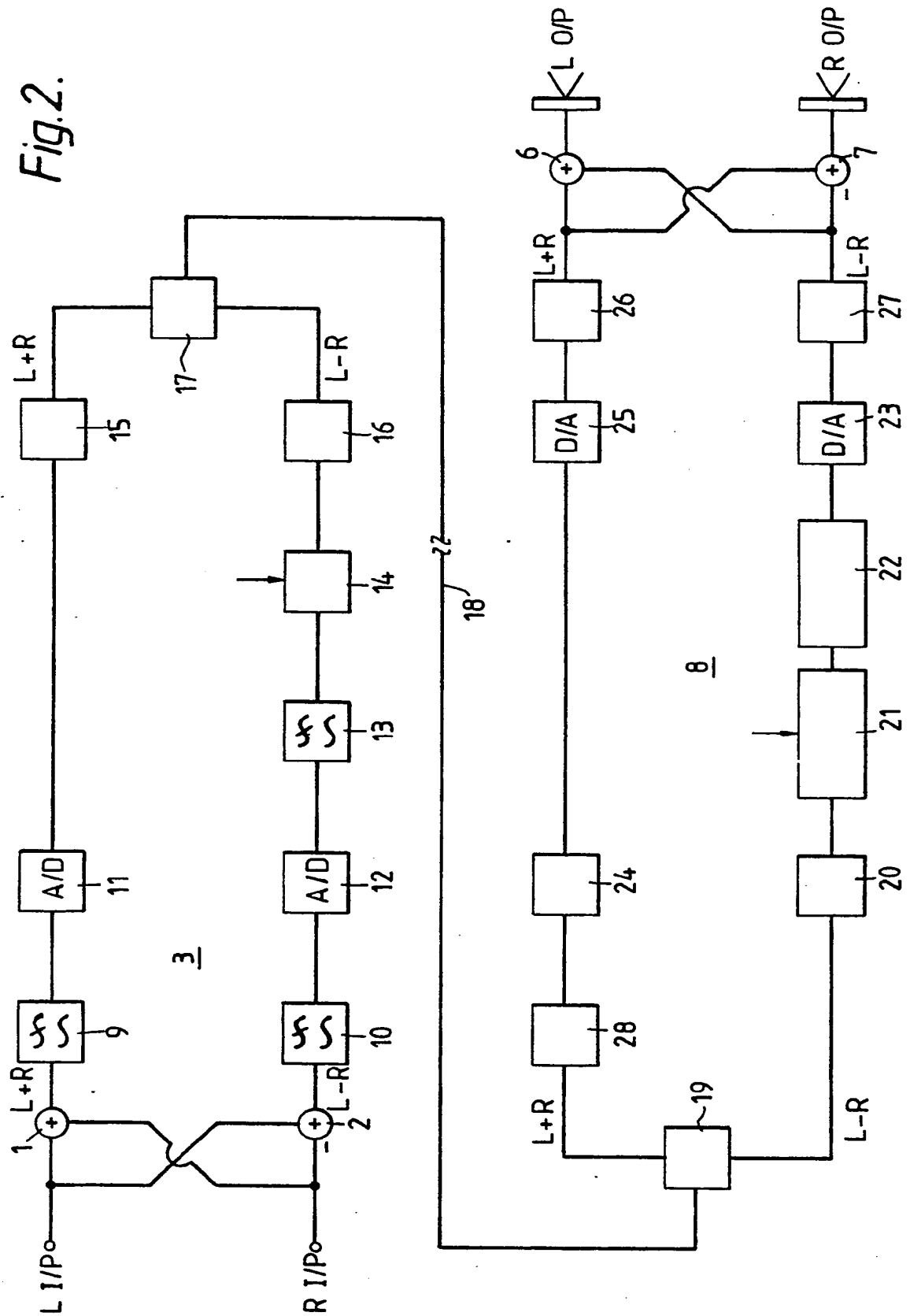
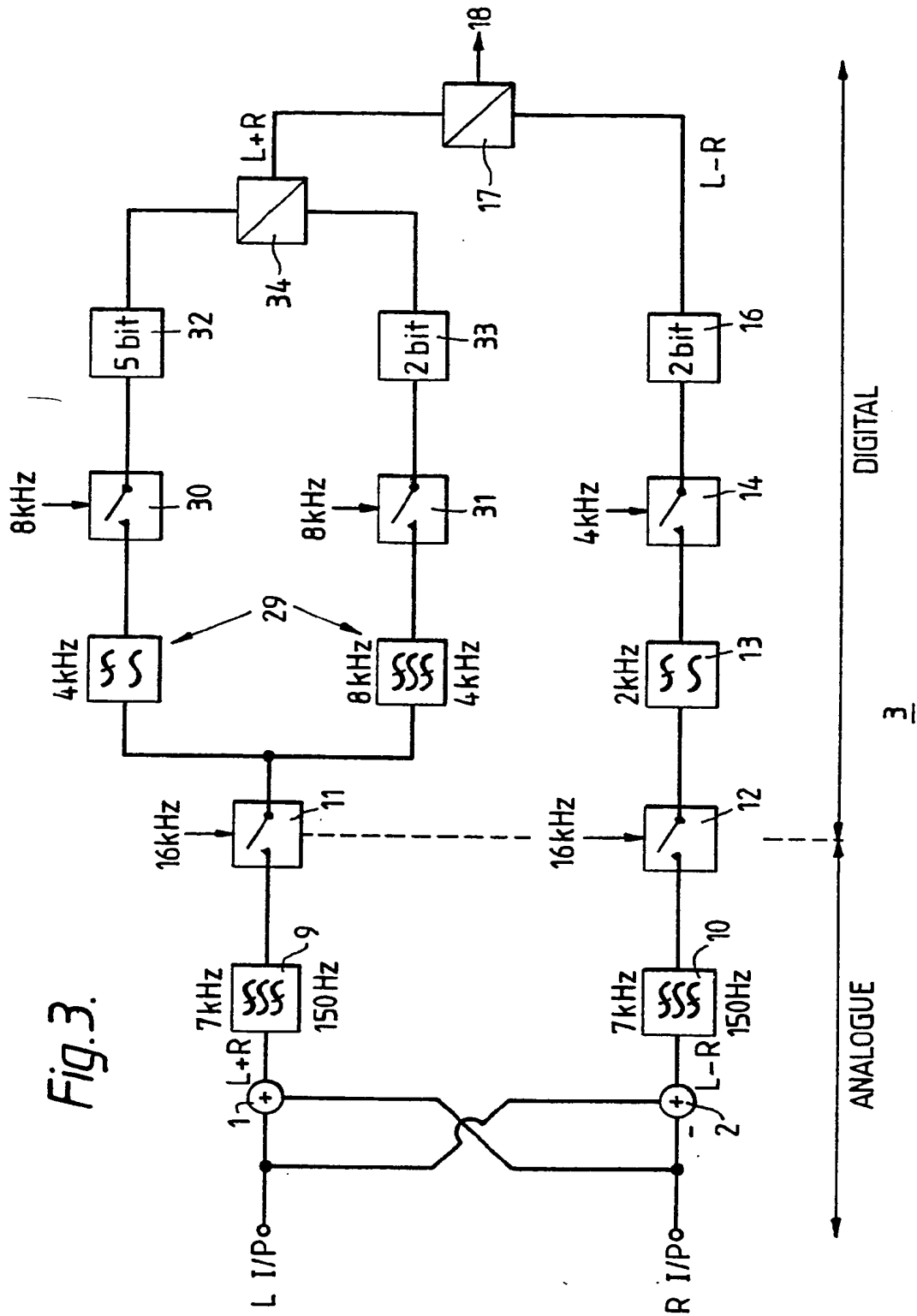
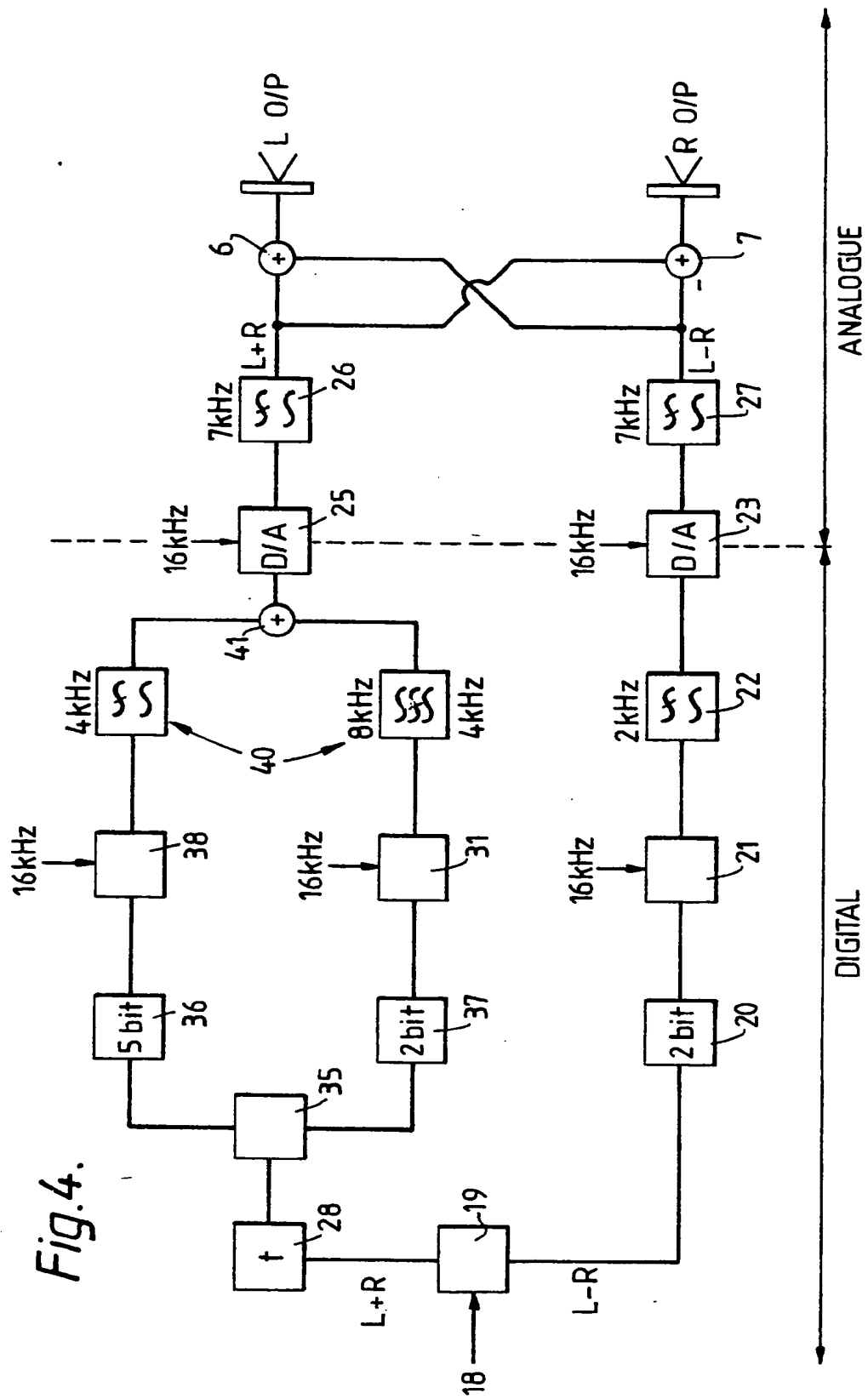
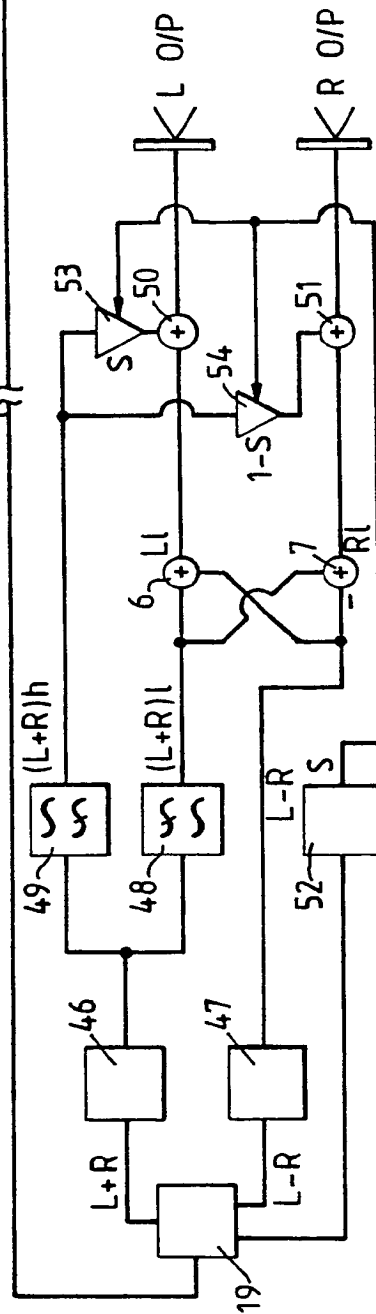
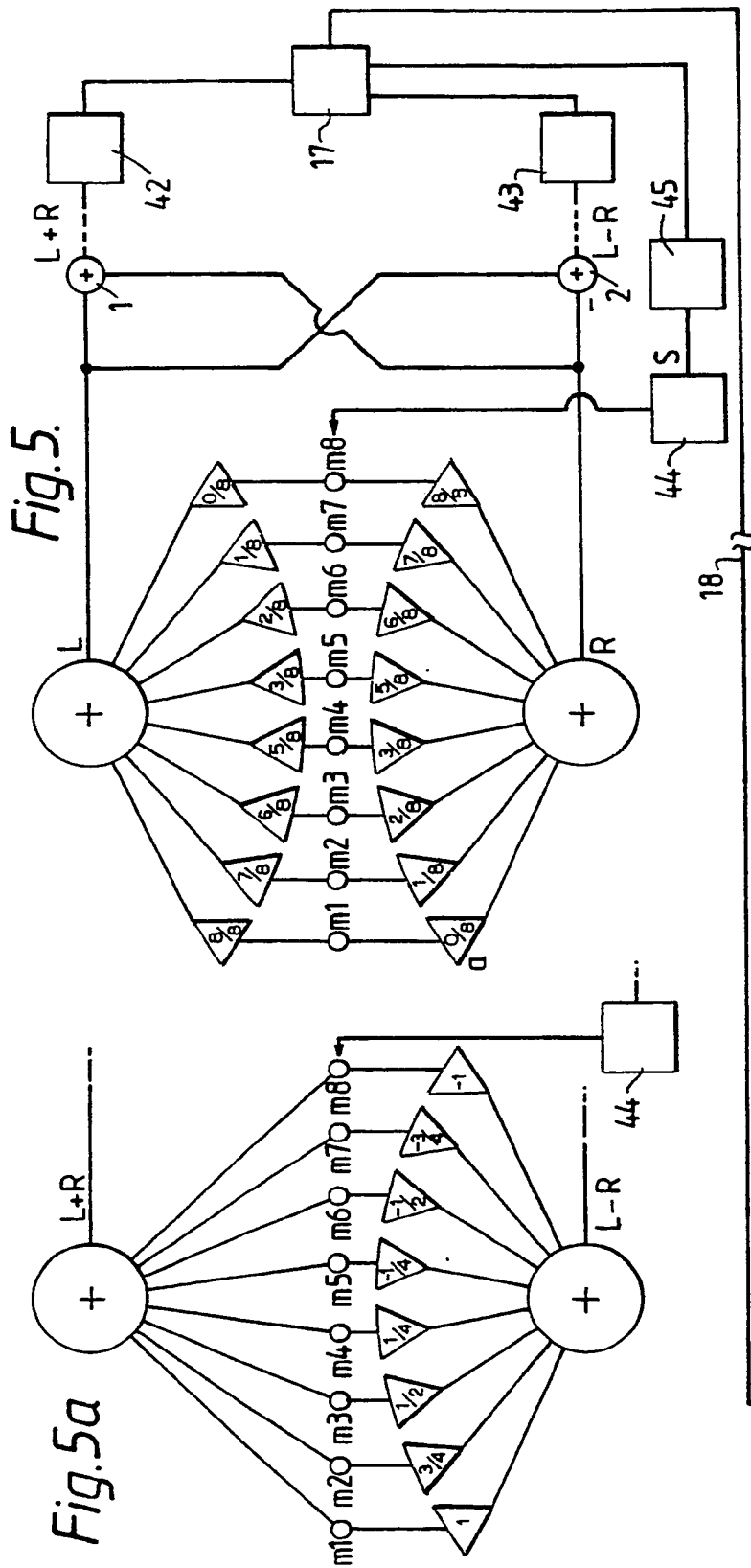


Fig. 2.











European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

EP 87 31 0140

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
A	EP-A-0 145 332 (BRITISH TELECOMMUNICATIONS) * Page 5, line 9 - page 7, line 10; figures 1-6 *	1,9	H 04 H 1/00 H 04 S 1/00 H 04 B 1/66
A	WO-A-8 500 686 (ADVANCED MICRO DEVICES) * Page 13, line 13 - page 18, line 22; figure 2 *	1,5,9	
A	THE BELL SYSTEM TECHNICAL JOURNAL, vol. 56, no. 5, May-June 1977, pages 747-769, American Telephone and Telegraph Co., New York, US; R.E. CROCHIERE: "On the design of sub-band coders for low-bit-rate speech communication" * Page 748, line 12 - page 750, line 15; figures 1,2 *	1,5,9	
D,A	GB-A- 970 051 (L.R. KAHN) * Page 7, line 7 - page 8, line 55; figure 7 *	1,3,17	TECHNICAL FIELDS SEARCHED (Int. Cl.4) H 04 B H 04 H H 04 S
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 02-03-1988	Examiner DELANGUE P.C.J.G.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1501 01.82 (P0401)

THIS PAGE BLANK (USPTO)